



AF/2112
App. Ser. No. 09/964,736
Att'y Docket No. 2207/11983
Assignee: Intel Corporation

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

OK to
Enter
NP
b2&104

In re application of:

Mark E. EIDSON

Examiner: Nimesh G. PATEL

Serial No. 09/964,736

Filed: September 28, 2001

Art Unit 2112

For: METHOD AND SYSTEM FOR
ATTACHING MULTIPLE CLOCK
SOURCES TO AN SDRAM ARRAY

RESPONSE TO OFFICE ACTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the final Office Action mailed October 6, 2004 in the above-identified application, the Applicant respectfully submits the following.

A Listing of Claims begins on page 2 of this paper.

Remarks begin on page 6 of this paper.